

<b>Notice of References Cited</b>	Application/Control No. 09/584,604	Applicant(s)/Patent Under Reexamination ROSENBERG, SCOTT A.	
	Examiner Javid A Amini	Art Unit 2672	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2001/0021971	09-2001	GIBSON et al.	712/215
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Prepetching in a texture cache architecture; Homan Igehy; Matthew Eldridge; Kekoa Proudfoot; ACM 1998 Stanford University, PP> 133-142.
	V	A Comparison of MPI, SHMEM and Cache-coherent Shared Address Space Programming Models on the SGI Origin2000; Hongzhang Shan and Jaswinder Pal Singh Department of Computer Science Princeton University, pp. 329-338.
	W	Co-Design of Interleaved Memory Systems Hua Lin and Wayne Wolf, Department of Electrical Engineering, Princeton University□□pp. 46-50.
	X	The authoritative dictionary of IEEE standards terms seventh edition, pages 426 and 1199.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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